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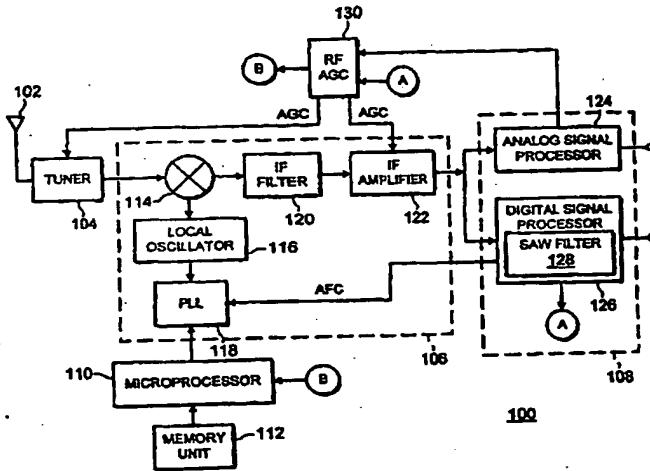
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(54) Title: TELEVISION RECEIVER FOR DIGITAL SIGNALS WITH OFFSET TUNING PROVISIONS



**WO 01/06768 A1**

(57) Abstract: A television receiver for receiving digital and analog signals that reduces adjacent channel interference when receiving digital signals susceptible to interference caused by a lower adjacent NTSC signal. Upon receiving the digital signal, the receiver heterodynes the digital signal with a local oscillator (LO) signal to produce an intermediate frequency (IF) signal. A microprocessor searches a memory unit for stored information regarding the digital broadcast channel and determines the presence or absence of a lower adjacent NTSC channel. In the case a lower adjacent NTSC channel is present, the microprocessor shifts the frequency of the LO signal causing the IF signal to shift towards the lower band edge of a surface acoustic wave (SAW) filter present in a digital signal processor further attenuating the lower adjacent NTSC channel.

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## TELEVISION RECEIVER FOR DIGITAL SIGNALS WITH OFFSET TUNING PROVISIONS

### FIELD OF THE INVENTION

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The invention generally relates to a digital television receiver. More particularly, the invention relates to an apparatus for selectively offsetting the frequency of a desired signal to obtain greater attenuation of undesired signals.

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### BACKGROUND OF THE DISCLOSURE

A Digital Television Standard published by the Advanced Television Subcommittee (ATSC) specifies vestigial sideband (VSB) signals for transmitting digital television (DTV) signals, such as high definition television (HDTV) signals. The VSB signals are transmitted in 6-MHz bandwidth television channels such as those currently used in conventional terrestrial broadcasting of National Television Subcommittee (NTSC) analog television signals within the United States. The HDTV system is incompatible with the NTSC broadcast standard, thus, if the broadcasting industry immediately adopted the digital HDTV system and abandoned the NTSC system, NTSC television receivers purchased within the last few years would be rendered obsolete. To avoid this undesirable result, the transition from conventional analog NTSC-standard broadcasts to digital HDTV television broadcasts will occur over a period that may last fifteen years to allow for normal attrition of older NTSC television receivers. Thus, during this transition period, both NTSC analog and HDTV digital signals will occupy the television spectrum. Television receivers manufactured during the transition period will be capable of processing both NTSC and HDTV signals.

At a particular geographic location, a receiver may receive signals from two transmitters that have adjacent channel spectrum allocations (e.g., an HDTV channel adjacent to an NTSC channel). When attempting to receive one of the signals (the desired signal), the other (the undesired, adjacent channel signal)

creates interference in the system. Consequently, the signals must be filtered to reduce the interference from the adjacent channel signal. This situation is particularly problematic when the desired signal is an HDTV signal and the undesired signal is a lower adjacent NTSC signal because of the proximity of the 5 aural carrier of the NTSC signal to the digital signal. The desired to undesired (D/U) signal ratio can be more than -40 dB. This presents an extreme challenge to fabricate surface acoustic wave (SAW) filters, such as those required by digital television receivers, that have a steep transition band roll off in order to remove the undesired signal without significantly attenuating the desired signal.

10 Thus, there exists a need in the art for digital television receivers using conventional SAW filters that are able to reduce adjacent channel interference, particularly, lower adjacent NTSC channel interference.

#### SUMMARY OF THE INVENTION

15 The invention overcomes the disadvantages associated with the prior art by providing a television receiver for reducing adjacent channel interference when receiving digital signals. Specifically, the invention tunes to a digital signal associated with a selected broadcast channel and downconverts the digital 20 signal to an intermediate frequency (IF) signal. The center frequency of the IF signal is determined by a microprocessor, which searches a memory unit for information associated with a selected broadcast channel and determines the presence or absence of a lower adjacent NTSC analog channel. In the case a 25 lower adjacent NTSC analog channel is present, the microprocessor causes the IF signal to be shifted by 62.5 kHz. The IF signal is thus pushed further towards the band edge of the surface acoustic wave filter present in the digital signal processor, resulting in further attenuation of the lower adjacent NTSC signal, particularly the aural carrier of the NTSC signal.

30 In an alternative embodiment of the invention, the microprocessor determines the input signal power of the tuned digital signal using data obtained from an automatic gain control (AGC) circuit. If the microprocessor determines the input signal power is larger than that of the adjacent channel, the

microprocessor does not cause the frequency of the IF signal to be shifted from nominal.

#### BRIEF DESCRIPTION OF THE DRAWINGS

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The teachings of the present invention can be readily understood by considering the following detailed description in conjunction with the accompanying drawings, in which:

FIG. 1 depicts a block diagram of a television receiver in accordance with  
10 the present invention;

FIG. 2A illustrates an IF spectrum and SAW filter response before  
frequency shift;

FIG. 2B illustrates an IF spectrum and SAW filter response after  
frequency shift; and

15 FIG. 3 illustrates the desired to undesired signal ratio for both nominal frequency and shifted frequency television signals.

To facilitate understanding, identical reference numerals have been used,  
where possible, to designate identical elements that are common to the figures.

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#### DETAILED DESCRIPTION

FIG. 1 depicts a block diagram of a television receiver 100 in accordance with the present invention. The receiver 100 comprises a tuner 104 coupled to an RF source 102, a frequency conversion stage 106, a demodulation stage 108, a microprocessor 110 coupled to a memory unit 112, and an automatic gain control (AGC) circuit 130. The tuner 104 selects the radio frequency (RF) signal corresponding to a broadcast channel selected from a plurality of channel locations in a frequency band provided by the RF source 102. The RF signals associated with broadcast channels are analog and digital television signals. The analog television signal may comprise a conventional National Television Standard Committee (NTSC) modulated signal within the United States. The digital television signal may comprise a Vestigial Sideband (VSB) modulated

signal in compliance with the Advanced Television Systems Committee (ATSC) standard A/53, for example, a high definition television (HDTV) signal.

The frequency conversion stage 106 comprises a mixer 114, a local oscillator (LO) 116, a phase-locked loop (PLL) 118, an IF filter 120, and an IF amplifier 122. The mixer 114 is coupled to the tuner 104 and receives the RF signal. The LO 116 is also coupled to the mixer 114. The LO 116 generates a LO signal having either a nominal frequency corresponding to the selected broadcast channel or a frequency shifted upward from nominal by 62.5 kHz. The PLL 118 controls the frequency of the LO signal using feedback from the demodulation stage 108 and signals from the microprocessor 110. The PLL 118 causes the LO 116 to generate a nominal frequency when the microprocessor 110 determines the absence of a lower adjacent analog broadcast channel. The PLL 118 causes the LO 116 to generate a frequency shifted upward from nominal by 62.5 kHz when the microprocessor 110 determines the presence of a lower adjacent analog broadcast channel.

The mixer 114 heterodynes the selected RF signal and the LO signal to produce a downconverted IF signal. The mixer 114 produces the sum and difference frequency products of the LO signal and the selected RF signal. The IF signal is coupled to the IF filter 120 that has a passband response selected to pass the difference frequency product of the LO signal and the selected RF signal. As shown in FIG. 2A, the spectrum of the filtered IF signal is a mirror image of the signal spectrum before downconversion. The IF amplifier is coupled to the IF filter and amplifies the IF signal for output to the demodulation stage 108.

When downconverting the selected RF signal, the frequency of the LO signal is determined using the following equation:

$$f_{LO} = f_c + f_{IF}$$

where  $f_c$  is the center frequency of the selected RF signal. When the microprocessor 110 determines the presence of a lower adjacent analog broadcast channel,  $f_{LO}$  is shifted upward from the nominal level by one 62.5 kHz (the resolution of the PLL 118). Thus, because the filtered IF signal is mirrored, the center frequency of the filtered IF signal,  $f_{IF}$ , is also shifted upward by 62.5 kHz.

In the United States,  $f_{IF}$  of the video carrier is nominally 45.75 MHz for analog television signals and 44 MHz for digital television signals.

- The demodulation stage 108 comprises an analog signal processor 124 and a digital signal processor 126 having a surface acoustic wave (SAW) filter 128.
- 5    The analog signal processor 124 is coupled to the IF amplifier 122 and demodulates and processes the IF signal for output. The SAW filter 128 of the digital signal processor 126 is coupled to the IF amplifier 122. The SAW filter 128 has a center frequency  $f_{IF}$  and has a passband designed to pass the inband digital signal and attenuate adjacent signals associated with both the upper and  
10   lower broadcast channels. The digital signal processor 126 is coupled to the SAW filter 128 and demodulates and processes the filtered IF signal for output.

The microprocessor 110 executes software stored in the memory unit 112 to determine if a lower analog signal adjacent the digital signal associated with the selected broadcast channel is present. Specifically, the microprocessor 110  
15   searches the memory unit 112 for information regarding the selected broadcast channel. If the selected broadcast channel is a digital channel, the microprocessor 110 determines the presence or absence of a lower adjacent analog channel. When a lower adjacent analog channel is absent, the microprocessor causes the frequency of the LO signal to be nominal. When a  
20   lower adjacent analog channel is present, the microprocessor 110 causes the frequency of the LO signal to be shifted from nominal by 62.5 kHz.

In an alternative embodiment of the invention, the microprocessor 110 receives signals from the AGC circuit 130 regarding the signal power of the selected RF signal. When the signal power is comparable to that of the lower  
25   adjacent analog signal, the microprocessor 110 performs the frequency shifting operation. When the signal power is larger than that of the lower adjacent analog signal, the microprocessor 110 does not perform the frequency shifting operation.

FIG. 2 illustrates the operation of the present invention when the selected  
30   broadcast channel is associated with an HDTV signal and both upper and lower adjacent broadcast channels are associated with NTSC signals. FIG. 2A shows the spectrum of the downconverted HDTV signal with both upper and lower

adjacent NTSC channels before the frequency shifting operation of the present invention. Because downconversion flips the spectrum of the IF signal, the lower adjacent NTSC signal has a higher center frequency than that of the selected HDTV signal. The dashed line indicates the frequency response of the SAW filter 128 of the digital signal processor 126. As shown, the aural carrier of the lower adjacent NTSC signal is within the passband of the SAW filter, which can cause interference in the demodulation and processing performed by the digital signal processor 126.

FIG. 2B shows the spectrum of the downconverted HDTV signal with both the upper and lower adjacent channels after the frequency shifting operation of the present invention. As shown,  $f_{IF}$  is shifted upward from nominal by 62.5 kHz thereby shifting the spectrum of the desired inband HDTV signal towards the upper band of the SAW filter 128. As a result, the aural carrier of the lower adjacent NTSC signal is shifted out of the passband of the SAW filter. FIG. 3 shows the effects of the present invention when receiving an HDTV signal in the presence of a lower adjacent NTSC signal for a plurality of broadcast channels.

Although various embodiments which incorporate the teachings of the present invention have been shown and described in detail herein, those skilled in the art may readily devise many other varied embodiments that still incorporate these teachings.

**What is claimed is:**

1. A television receiver for receiving a digital television signal susceptible to  
5 inference caused by a lower adjacent analog television signal, said television  
receiver comprising:

a tuner for receiving the digital signal associated with a broadcast channel  
selected from a plurality of channel locations in a frequency band;

10 a frequency conversion stage, coupled to said tuner, for converting in  
frequency the digital signal to an intermediate frequency (IF) signal to be output,  
where the center frequency of said IF signal is capable of being switched to a  
nominal frequency or to a second frequency being different than said nominal  
frequency; and

15 a filter, coupled to said frequency conversion stage, for attenuating  
adjacent signals, said filter having a center frequency equal to said nominal  
frequency;

where said filter, in response to the center frequency of said IF signal  
being switched to said second frequency, further attenuates a lower adjacent  
analog signal.

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2. The television receiver of claim 1 further comprising:

a memory unit for storing software and information associated with each of  
the plurality of broadcast channels; and

25 a microprocessor, coupled to said memory unit and said frequency  
conversion stage, for executing software stored in said memory unit, searching  
said memory unit for a lower analog signal adjacent to the digital signal, and  
causing the center frequency of said IF signal to be switched to either said  
nominal frequency or said second frequency;

30 where said microprocessor, in response to the absence of a lower analog  
signal adjacent to the digital signal, causes the center frequency of said IF signal  
to be switched to said nominal frequency;

said microprocessor, in response to the presence of a lower analog signal adjacent to the digital signal, causes the center frequency of said IF signal to be switched to said second frequency.

- 5 3. The television receiver of claim 1 wherein said frequency conversion stage comprises:

a local oscillator for generating a local oscillation signal;  
a phase-locked loop, coupled to said local oscillator, for controlling the frequency of said local oscillation signal;  
10 a mixer, coupled to said local oscillator, for heterodyning the digital signal with said local oscillation signal to generate said IF signal; and  
an IF filter, coupled to said mixer, for passing the lower band of said IF signal.

- 15 4. The television receiver of claim 1 wherein said second frequency is said nominal frequency shifted upward by 62.5 kHz.

5. A method of receiving a digital television signal susceptible to interference caused by a lower adjacent analog television signal comprising the steps of:  
20 tuning a radio frequency (RF) signal having a digital signal inband and a lower adjacent analog signal;  
offsetting the frequency of a local oscillator (LO) signal;  
heterodyning said RF signal with said LO signal to generate a modified intermediate frequency (IF) signal having a frequency offset from nominal; and  
25 filtering said modified IF signal to attenuate said lower adjacent analog signal.

6. The method of claim 5 wherein offsetting the frequency of said LO signal comprises shifting the frequency of said LO signal upward by 62.5 kHz.

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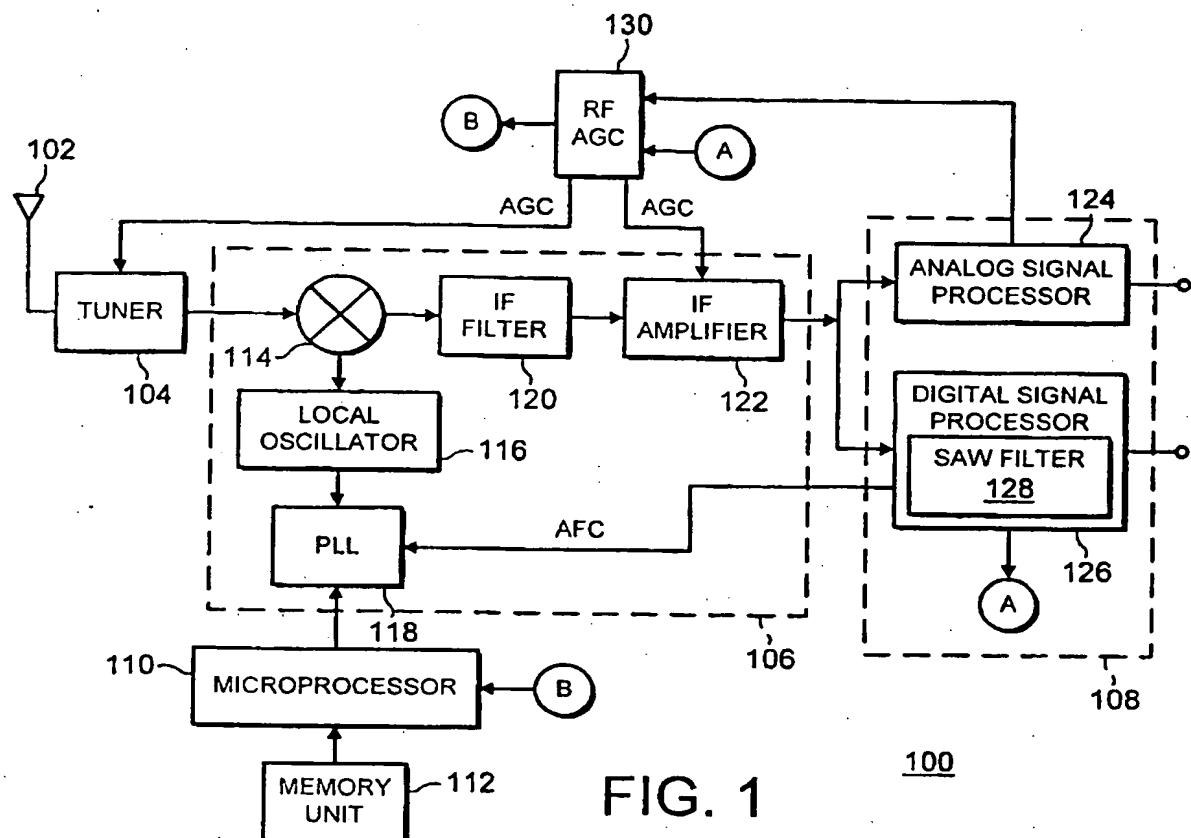


FIG. 1

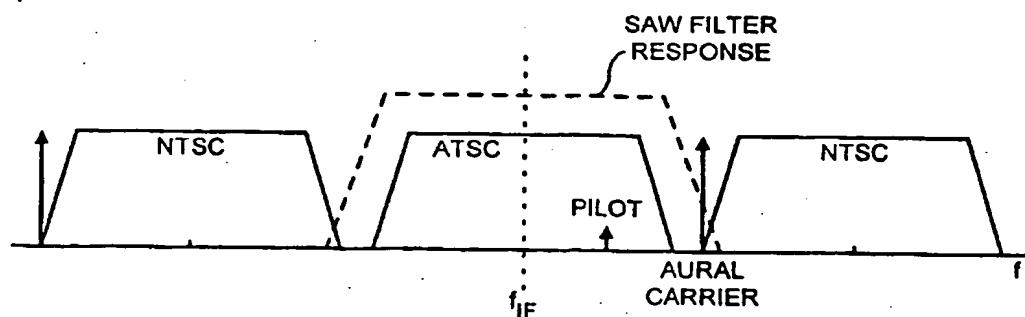


FIG. 2A

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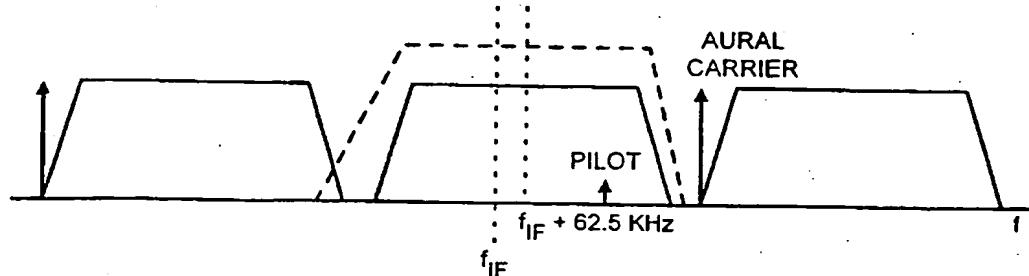


FIG. 2B

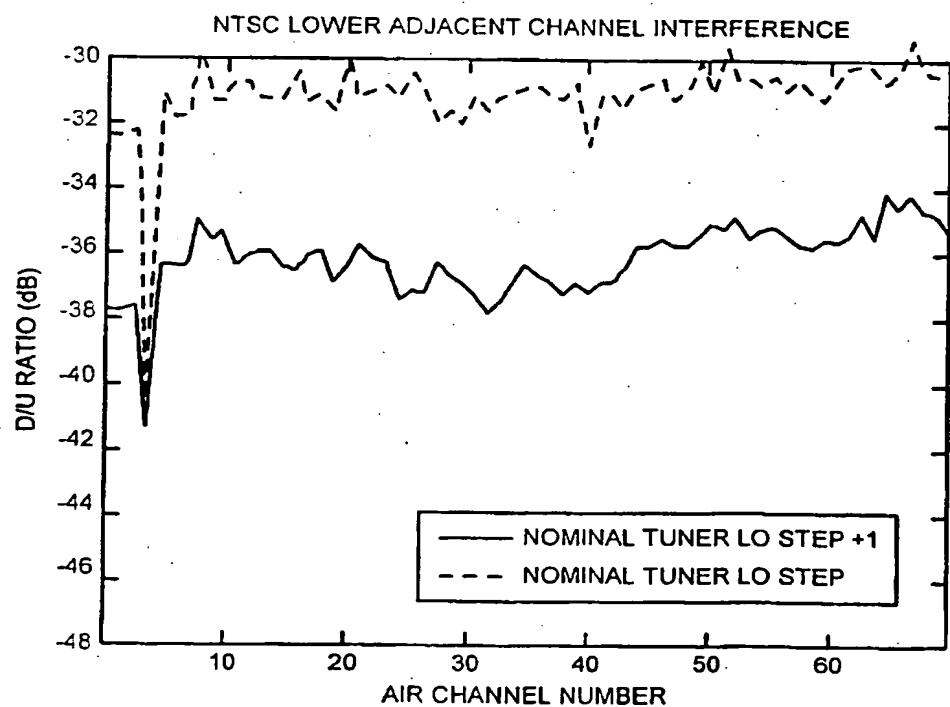


FIG. 3

SUBSTITUTE SHEET (RULE 26)

# INTERNATIONAL SEARCH REPORT

Intern'l Application No  
PCT/US 00/19103

**A. CLASSIFICATION OF SUBJECT MATTER**  
IPC 7 H04N5/44 H04N5/21

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)  
IPC 7 H04N

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

WPI Data, EPO-Internal, PAJ

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	EP 0 903 937 A (ALPS ELECTRONIC CO LTD) 24 March 1999 (1999-03-24) column 1, line 25 -column 2, line 26	1,5
P,A	US 5 940 143 A (IGARASHI Y. ET AL) 17 August 1999 (1999-08-17) the whole document	1,5
A	& JP 09 163265 A (HITACHI LTD.) 20 June 1997 (1997-06-20)	
A	US 5 805 241 A (LIMBERG A.) 8 September 1998 (1998-09-08) column 5, line 32 -column 6, line 22	1,5
A	WO 99 05796 A (KONINKLIJKE PHILIPS ELECTRONICS N.V.) 4 February 1999 (1999-02-04) page 6, line 18 -page 8, line 14	1,5
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Patent family members are listed in annex.

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Date of the actual completion of the international search

2 November 2000

Date of mailing of the international search report

10/11/2000

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## INTERNATIONAL SEARCH REPORT

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## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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Form PCT/ISA/210 (continuation of second sheet) (July 1992)

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Information on patent family members

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